



GAU 2822

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wendell P. Noble et al.

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Docket No.: 303.379US2
Filed: April 17, 2000
Examiner: Michael Trinh

Serial No.: 09/551,027
Due Date: April 30, 2001
Group Art Unit: 2822

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ Response to Non-Compliant Amendment (Amendment and Response filed on March 21, 2001) (12 Pages).
- ☒ Clean Version of Specification Paragraphs (3 pgs.).
- ☒ Clean Version of Pending Claims (8 pgs.).

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: Edward J. Brooks, III
Atty: Edward J. Brooks, III
Reg. No. 40,925

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 24 day of April, 2001.

Name Amy Moriarty

Signature Amy Moriarty

Customer Number 21186

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